

Phillips SA8016DH 2.5 GHz Synthesizer SEE Testing

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To be presented by Marty Carts at the 2008 Single Event Effects Symposium, Long Beach, CA, 15 April 2008

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Outline



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 - SA8016 Frequency Synthesizer
 - Prior SEE Test Results
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- Measures of Performance
 - SET
 - · SEU
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- Data Analysis
- Discussion and Summary

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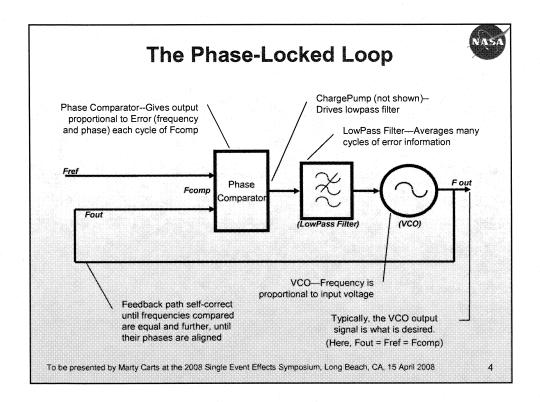
Introduction

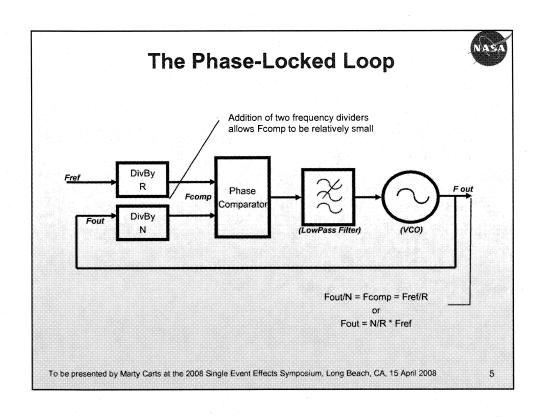


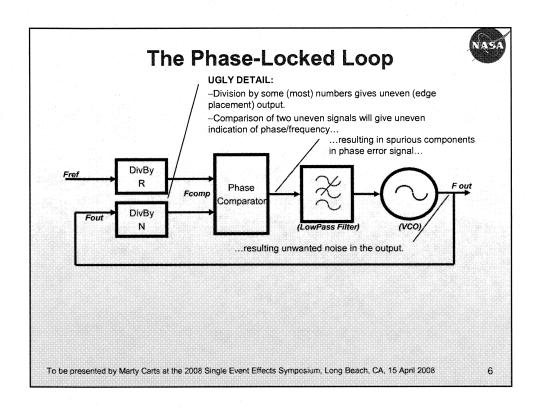
- SA8016 Contains All Major Components of a Phase-Locked Loop (PLL) Except the VCO and Loop Filter (Capacitor)
- Built in Phillips QUBiC II BiCMOS Process
- Chosen by GLAST Program for Frequency Generation in both Tx and Rx
- Preliminary Testing Indicated Possible Problem
- GSFC Followup Testing Aimed at Duplicating Test (Hardware, Methods) and establishing adequate statistical knowledge of any problems.

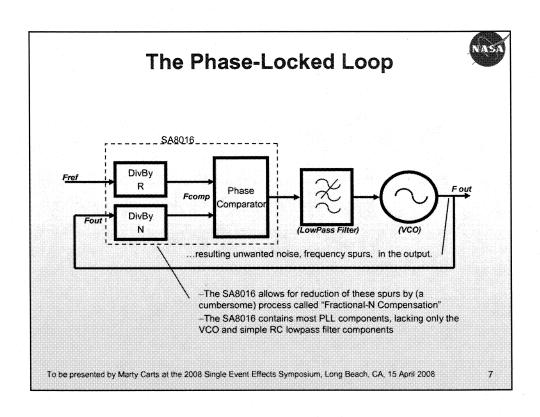
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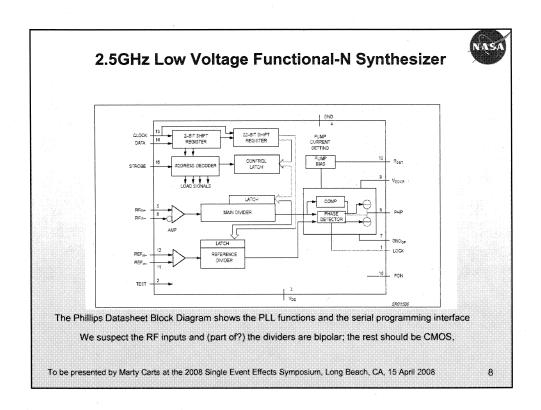
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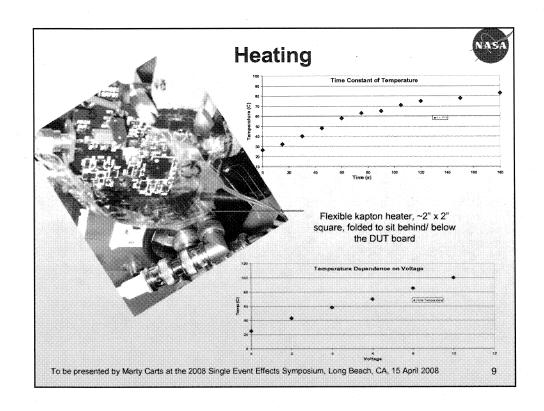


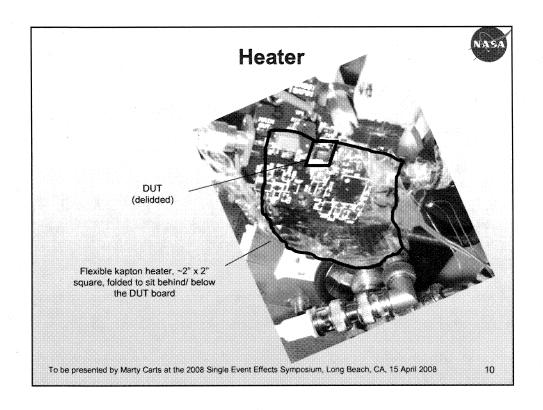


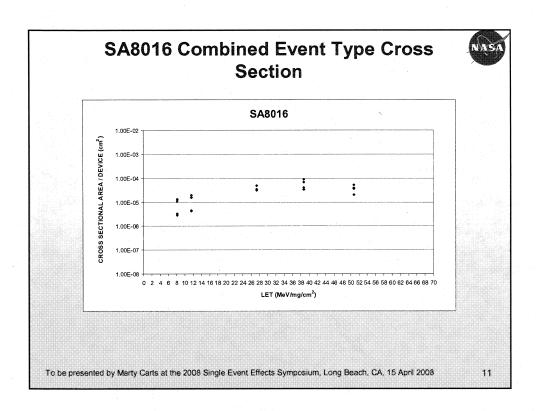












Discussion and Summary



- This device experiences these SEE:
 - Many transient phase excursions expected to obliterate short (< 1 ms) data;
 - Some temporary frequency offsets and major phase excursions expected to obliterate longer (< 10 sec) data;
 - SEFI which will obliterate data until reconfiguration reestablishes the correct frequency;
 - No destructive types of events;
 - SEFI which require power cycle/reconfig. In applications which cannot powercycle this is effectively a destructive event.
- The judgement as to whether to use or not use this device will depend on the ability to powercycle or tolerate a destructive condition rate calculated for that part.

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